JOH MAN

220025CZ

VACUUM FLUORESCENT DISPLAY MODULE

SPECIFICATION

1-30

MODEL

; CU200285CPB-\$20A

99 -40 - +80

-20+65 1 KIEEK

CUSTOMER

SPECIFICATION NO.

:T-CU20026SCPB-S20A-R1

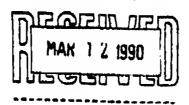
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CU208268CPB-320A-1

- 1.0 General Description
- 1.1 Application : Resdout of computer: micro-computer, communication terminal and automatic instruments.
- 1.2 Construction: Single board display module consists of 40 character VPD, refresh memory, character generator, control circuit and DC/DC converter.
- 1.3 Display color : Blue-green.
- 1.4 Outline dimension : See attached drawings.
- 2.0 Absolute Meximum, Retings

Power Supply Voltage ---- Vec : +7.0 Max. Vpc Logic Input Voltage ---- Vin : +7.0 Max. Vpc

3.0 Blectrical Ratings

Parameter	Symbol .	Min.	Îvo.	Max.	<u>Nolt.</u>
Power supply voltage	Vee	4.75	5.80	5.25	Vac

4.8 Riectrical Characteristics

PARAMETER		SYMBOL	HIN	TYP	MAX	UNIT	COND
INPUT VOLTAGE	В	Vin	2.2	•	Vec	Vpc	Vcc=5.0V
	L	ViL	•	-	0.8	Vpc	Vec=5.0V
OUTPUT VOLTAGE	H	Voн	2.4	-	-	Voc	Ion=-400 # A
	L	VoL	-	-	0.45	Voc	IoL=1.6=A
SUPPLY CURRENT		Icc	-	0.3	0.4	A	Vec=5.BV Operate all dota in all chr positions

Note:

Power-on delay of Vcc shall be within 30 ms.

Icc might be anticipated more than 2 times figure of above table at power on rush.

4

5.0 Optical Specifications

Number of cherecture : 48(2 line x 20 chre)

Hatrix format : 5 x 7 dot character with cursor

Display area : 108.2 wm x 16 mm(X x Y) Character size : 3.3 mm x 5.05 mm (X x Y) Character pich : 5.1 mm(center-to-center) Dot alse : 0.5 mm x 0.55 mm(X x Y) Dot pitch $: 0.7 \text{ mm} \times 0.75 \text{ mm}(X \times Y)$

Luminance : 280 fL(Typ.)

Color of illumination : Blue-green

6.0 Bovironmental Specifications

Operating temperature : -20 to +80°C Storage temperature : -48 to +78°C Operating howidlty : 20 to 80 % R.H.

7.0 Mechanical Strength

Vibration Test : 10-55-10 HZ : Prequency

Sweep time : 1 minute

Amplitude : 2 we (Fixed 18G)

: X.Y. & Z (3 directions) Direction Times : 30 Min. for each direction

Shock Test : Acceleration : 100G

> Duration , : 9.0 maec

Direction : X,Y & Z (3 directions) Times : Three (3) times for each

direction

The test shall be done at no operating and no any mechanical and electrical failures should be found after the tests.

8.0 Functional Descriptions

The CU200265CPB-S20A VFD Module will provide the functions of DATA WRITE. COMMAND WRITE, STATUS READ and DISPLAY RESET.

WR	RD	AO	CS	PUNCTION	DIRECTION OF DATA BUS
0 1	1	٥	0	DATA WRITE	HOST TO MODULE
0-1	1	1	0	COMMAND WRITE	HOST TO MODULE
1	C	1	0	STATUS READ	MOBULE TO HOST

- 8.1 Data write.
- 8.1.1 Data write is executed at rising edge of WR pulse while CS-A8="0" and RD="1". This module accepts 158 ASCII characters and 16 control codes listed in Table 1. Five desired fonts may be siternated into character code of 00 Hex to FF Hex in Table 1 with ESC(18 Nex) code. See(16) ESC. Generally, the cursor sutomatically moves to right by one character position after execution of data write.

Control code are defined as follows:
(The term of "CURSOR" means the writing position.)

- 1) BS: Back Space
 - DC1 Mode: The cursor position is shifted to the left by one character position.

 When the cursor is located at the left end of the bottom line, the cursor is shifted to the right most position of the top-line after execution.

 When the cursor is in the left sost position of the top line, the cursor is shifted to the right most position of the bottom line.

 DC2 Mode: Same as DC1 Mode.
- 2) HT: Horisontal Tab
 - BC1 Mode: The cursor position is shifted to right by one character position.

 When the cursor is located at the right end of the top line, the cursor is shifted to the left most position of the bottom line.

 When the cursor is on the righ; most position of bottom line, the cursor is shifted to the left most position of the top line.
 - DC2 Mode: When the cursor is on the right most position of the bottom 'lne, all characters on the bottom line are shifted to one 'ine up. and cursor is positioned to the left most position of the bottom line. At this time, all positions of the bottom line are counted for a new line.
- 3) LF: Line Feed
 - DC1 Mode: The cursor is shifted to the same column position of next line.

 When the cursor is on the bottom line, the cursor is shifted to the same column position of the top line.
 - DC2 Mode: When the cursor is on the bottom line, sil characters on the bottom line are shifted to the upper line, and the cursor saintains the same position of the bottom line. At this time, all-positions of the bottom line are cleared for a new line. When the cursor is on the top line, same as DC1 Mode execution will be made.
- 4) CR: Carriage Return
 - DC1 Mode: The cursor is positioned on the left most position of the same line.
 - DC2 Mode: Samm as DC1 Mode.

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5) DC1: Norwal Hode (Default Hode)

After a character is written, the position of the cursor is sutomatically shifted to the right by one character position. When the cursor is on the right most position of the top line, the cursor is shifted to the left most position of the bottom line. When the cursor is on the right most position of the bottom line, the cursor is shifted to the left most position of the top line.

6) DC2: Scroll Mode

After all positions of the bottom line are written, the characters written on the bottom line are scrolled up to the top line, and the cursor is positioned at the left most position of the bottom line.

At this time, all characters on the bottom line are cleared for a new line. The display module autometically selects the BC1 Mode above at Initial power-on time. This selection will be maintained enother mode will be selected.

7) DC3: Cursor On Mode (Default Mode)

The cursor position is displayed as an under-line.

8) DC4: Block Cursor Node

The character on cursor position is alternatively flickering with full dots.

9) DC5: Cursor Off Mode

The under-line on cursor position is becoming invisible and BC4. BC6 Mode are cancelled.

18) BCS.: Cursor Blink Mode

, The under-line on cursor position is flickering.

The following five control codes select the fost as follows:

- 11) SUB: English font (USA ASCII-?) (Default Code)
- 12) FS : Baniah font (ECMA-7)
 - 13) GS: Gameral Buropess font (BCMA-7)
 - 14) RS : Swedish font (ECMA-7)
 - 15) US : German foat (BCMA-7)

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Conversion table from ASCII to ECHA is shown as follows:

ł	•	C	ONVERSION COS	ts .	
HEX CODE	16	10	10	10	1F
23	#	#:	#.	1	£
52		Æ		 	Fi
SC	٠.,	Ø	14.	ij	i <u>"</u> i
50]	Ĥ]	i]
5E	٠٠٠.	.**.	,	ij	i_i
78		£6 <u>7</u>		ä	ä
7C		i Ci	1	Ö.	Ö
70		:::	}	÷	ü
70	,****	,	,449,	三道	Ë
	RBCII	DANISH	GEN EUROPE	SHEDISH:	GERHAN

SUB(1A Nex). English font, in autometically selected at the power-on or reset. The selected mode is maintained unless other mode is selected.

16) BSC: The following BSC code assigns five user desired fonts (UDF) into any character positions from DD Hex to FF Hex of table 1. RAH of the module reserves five-character-size of memory for these new characters,

Six-byte data succeding this ESC code siternates present character font to new font desired.

lat byte : 18 Hex

2nd byte : Definition character code

Definable character codes are available from DD Hex to PP Hex of table 1. If the character code of control characters as BS.HT, CR.etc. is selected for new character, the module displays new character instead of control action. Caution that definition of 18 Hex (ESC) character code kills BSC function thereafter.

3rd--7th byte: Formation of character font

Each dot data of 5 x 7 is defined with following Table.

Figures in the Table are correspond to each dot position of 5 x 7. The dots to be lighted shall be specified as "1" (active high).

BYTE	D7	D6 -	D5	04	D3	D2	D1	DS
3rd	22	14	21	3	20	2	19	1
4 th	UL	8	25	7	24	6	23	5
5th	29	12	28	11	27	10	28	8
6th	33		32	15	31	14	30	13
7th		*	•		35	18	34	17

=="0" (low) UL: Under line

After execution of above sequence, new character defined will be displayed by defined character code.

DISPLAY DOT

. 1	2	3	4	5
6	7	8	9	11
11	12	13	14	15
16	17	18	19	21
21	22	23	24	2.5
26	27	28	29	31
31	32	33	34	35

5 X 7 DOT

Example:

Definition of new character "!" to character code AS Hext

Dot pattern

0	•
0	
0	
. 0	
0	

5 X 7 Dot

Specify each dot

Byte/Bit	7	6	5	4	3	2	1	0	HBX
3rd Byte	0	0	0	1	0	8	B	0	10
4th Byte	0	1	0	8	8	0	8	0	40
5th Byte	0	0	G	0	0	0	0	0	00
6th Byte	1	0	0	Û	0	0	. 0	1	81
7th Byte	Û	0	0	0	C	1	8	0	04

Then Syntax should be written; 18 + 48 + 18 + 40 + 68 + 81 + 04 (Hex)

8.2 Command Write

Command write is executed at rising sage of WR pulse while CS="0" and A8=RD="1". This wildle provides following commands:

80XX XXXX: Set the cursor on 80XX XXXX (Hex) position.

8040 0808 (80 Hex): The left most of the top line

8040 0808 (001 Hex): The 2nd column of the top line

8041 0811 (13 Hex): The right most of the top line

8041 0801 0801 (14 Hex): The left most of the bottom line

8040 0811 (27 Hex): The right most of the bottom line

8040 0811 (27 Hex): The right most of the bottom line

8040 0810 0811 (27 Hex): The right most of the bottom line

8040 0810 0810 (14 Hex): The right most of the bottom line

8040 0800 0800 (14 Hex): The right most of the bottom line

8040 0800 0800 (14 Hex): The right most of the bottom line

8040 0800 (14 Hex): The right most of the bottom line

8040 0800 (14 Hex): The right most of the bottom line

0100 000B: (40 Hex) Software reset

Same execution as hardware reset of 8.4

8.3 Status Road

The wodule outputs the status on bit 1 of data bus, when CS-RD-"9" and A0-WQ-"1". Sit 0: Do not care

317 1: Status of data write: data write and command write are valid only when BiT land.

BIT 2 ibrough 7: Do not care.

we confirming of status bits, however, is needed, only when the period of the cycle is longer than 1.8 ms.

8.4 dardware Reset

QESE[="1" Makes the module initialized se follows:

- i. All character positions are filled with SP(20 Hex) characters.
- 2. The cursor position is set on the left most position of the top line.
- 3. DCI and DC3 modes are selected.
- i. Alternated characters specified by BSC code are cancelled, and standard characters in character generator are selected.

Reset signal is active high and shall be maintained 50 ms or longer. No incret is executed within 100 ms after reset pulse or reset command. (SBE I MING CHART)

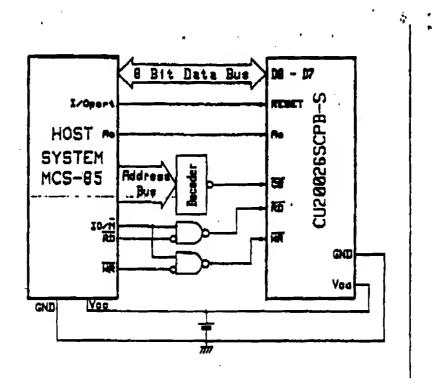
8.5 Test Mode

"O" more than Adesec to the TO line at the power on or reset may initiate the test rode. Ing the test wode, as any date/commands are acceptable. The test mode can be cannot at only power off or mast at open of TO line. All stored BON character fonts are disc ayed automatically at this mode.

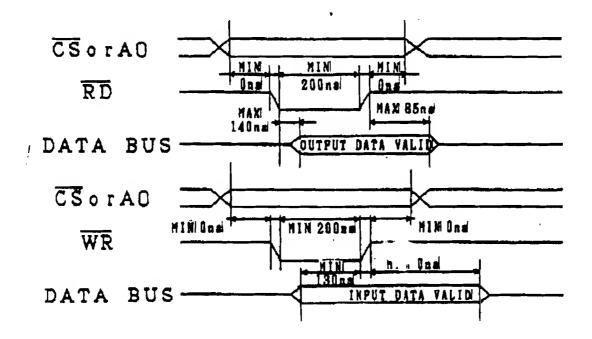
CHA	IRF	101	(EF	5	F	7	10	1	TS	5	•	÷	•				•
	D7 D6 D5 D4	3 0 0	.g g g	8	8 0 1	9 9	1 8	1 1 0	8 1	9 9	1 0 0	1 2	1 1	1 9 6	101	1 2	1 1
03 DS D1 D0		è	1	2	3	4	5	S	7	8	9	A	8	С	ם	Ε	F
0 0 0 0	8			SP	ij		-	•	É				****	.7	111		
8881	1		DCI	i	.i.	H		111	įΨ			r.i	1	7:	<u>:</u> i		
0010	2		DCS	11	 	E		1	!- ~.			ŧ		1			
8811	3		DC3	#	· ·	1	1	1	•:=:			. .i	,	;;, ;,,	::::		
8188	4		DC4	*	ᆦ			1	+:			٠,	I	þ .	1:3		
0101	5		DC5	15 ,	II. II	E			11			#	ः	;	1		
8118	6		DCS			F	1,1	+	1.,1			=	<u>†</u> †	•••	1		
8111	7			;1	1		<u> </u>		1,.1			<u></u>	丰	;;;'	, 'I		
1000	8	æ					\mathbf{x}_{i}	-	:			·1	7	·•; ·•;	1		
1881	9	Hī				Ţ	ii	i	' , '			باندو اباندو		.1	11.	· · · · ·	
1010	R	LF	SUB	lsp:	# a			1				工		ľ	[]		
1811	B		ESC	.	;	<u> : : </u>		: :	:			::f:	11	 			
1100	C		FS	;1	٠,		٠,	1				†;1	:		r;		
! 101	D	CF	Œ		-100 -170	[1]	7	[1]	11.					•••	• •	j	لــــــــــــــــــــــــــــــــــــــ
1110	Ε		RS	:1			,•••,	†" 1	,177			Ħ	1,1	1 :			·
1111	F		US	,,,,,	•"			ii				. ; , }		*.*	i::1		

Table 1

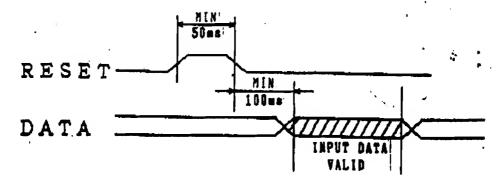
9.0 Interface Example



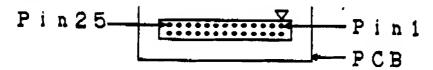
10.0 Data Write / Reed Timing



11.0 Reset Timing



12.0 Pin Connection



PIN NG.	SIGNAL
1 .	D 7
3	D 6
5	D 5
7	D 4
9	D 3
1 1	D 2
1 3	D 1
1 5	D O
1 7	WR
1 9	A 0
2 1	RD
2 3	CS
2 5	Т 0

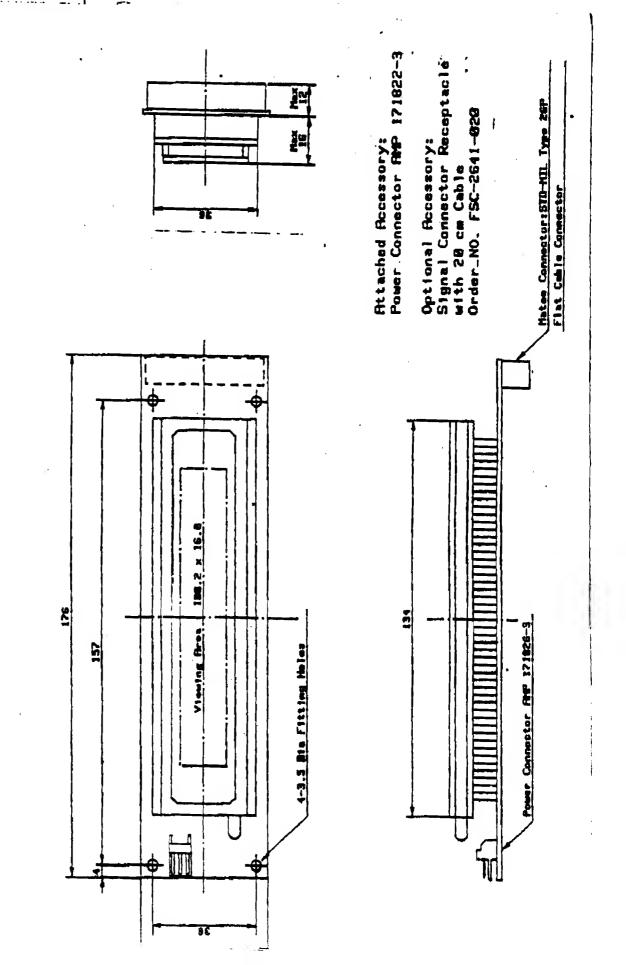
PIN NO.	SIGNAL
2	GND
4	GND .
6	дир
8	GND
1 0	GND
1 2	GND
1 4	GND
1 6	GND
1 8	GND
2 0	RESET
2 2	GND
2 4	GND
2 6	GND

3 2 1 1 : V c c

2 : N C

3 : GND

CU20026SCPB-S20 OUTLINE DIMENSION



IMPORTANT PRECAUTIONS

- All VPD Modules coatein MOS LSIs or ICs. Anti-Static handling procedures are always required.
- * VF Dieplay consists of Sods-lies glass. Heavy shock eors than 100 G. thermal shock greater than 10 °C/minute, direct hit with hard material to the glass surface -- especially to the EXHAUST PIPE -- may CRACK the glass.
- * Do not PUSH the display atrongly. At mounting to the system frace, alight gap between display glass face and frost panel is necessary to avoid a contect failure of lead pine of display. Twist or warp counting will make a gless CRACK around the lead pin of display.
- * Neither DATA CONNECTOR or POWER CONNECTOR should be connect or disconnect while power is applied.

 As is often the case with most subsystems, caution should be exercised in selectively disconnecting power within a computer based system. The modules raceive high logic on strobe lines as rendom signals on all data ports.

 Removal of primary power with logic signals applied any damage input circuitry.
- * Strasses more then specification listed under the Absolute Maximum Retings may cause PARMANENT DAMAGE of the modules.
- * +5 volts power line must be regulated completely since all control logics are depended on this line.

 Do not apply slow-start power. Provide sufficient output current power source to avoid trouble of RUSH CURRENT at power on. (At least output current of double figure of Icc. listed on the epecification of each endules, is required.)
- . Date cable length between codule and host system is recommended within 288 co to free from a mis-operation caused by noise.
- * Do not place the endule on the conductive plate just after the power off
 Due to big capacitors on the endule, core than 1 cin. of discharging time 's
 required to avoid the failure caused by shorting of power line.